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of the SPI interface followed by an introduction to Analog Devices' SPI enabled switches and muxes, and how they help reduce the number of digital GPIOs in system board design. SPI is a synchronous, full duplex master-slave-based interface. The data from the master or the slave is synchronized on the rising or falling clock edge.

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the basis for a two course sequence in communication systems, or a single course on digital com-munication, at the undergraduate or beginning graduate level. The book also provides a review or introduction to communication systems for practitioners, easing the path to study of more advanced graduate texts and the research literature.

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Analog & Digital Signals Analog & Digital Signals 2 This presentation will •Review the definitions of analog and digital signals. •Detail the components of an analog signal. •Define logic levels. •Detail the components of a digital signal. •Review the function of the virtual oscilloscope. Analog and Digital Signals Analog Signals